

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	SYSTEM AND METHOD FOR PROTECTING AGAINST SHORT CIRCUITS IN ELECTRIC POWER DISTRIBUTION ARCHITECTURES WITH TWO VOLTAGE LEVELS							
Application Number : Date : First Named Applicant: Carles Borrego Bel Confirmation Number: Attorney Docket Number: 8153ES								
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td><td>Sign. Capacity</td></tr><tr><td>Bruce E Harang Registered Number: 29720</td><td>Bruce E Harang</td><td>Attorney</td></tr></table>			Submitted By:	Elec. Sign.	Sign. Capacity	Bruce E Harang Registered Number: 29720	Bruce E Harang	Attorney
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Bruce E Harang Registered Number: 29720	Bruce E Harang	Attorney						

Documents being submitted:	Files
us-fee-sheet	8153ES-usfees.xml us-fee-sheet.xml us-fee-sheet.dtd
us-ids	8153ES-usidst.xml us-ids.dtd us-ids.xml
us-request	8153ES-usrequ.xml us-request.dtd us-request.xml
application-body	8153ES-trans.xml us-application-body.xml application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd 8153ES_Fig_1.tif 8153ES_Fig_2.tif 8153ES_Declaration_1.tif 8153ES_Declaration_2.tif
us-declaration us-declaration	
Comments	